IN THE SPECIFICATION

At page 8, please make the following amendments to paragraphs 0024:

Device 105 may include a first processor 120 and a second processor 125. Device 105 may be a physical processor. Device 105 may also be an embedded system or other device having at least two processors. Processors 120 and 125 may be logical processors. For example, processor 105 may include architecture state registers 130 and 135 that each holds a unique architecture state. It is readily apparent that device 105 may include more than two logical processors that each have an architecture state register associated with it to hold a separate architecture state. The two processors 120 and 125 share the same execution resources 140, caches 145, ~~bus~~ busses 115/150, and memory 155.